# **BGF200**

Microphone Filter and ESD Protection

**Small Signal Discretes** 



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| BGF200  |  |
|---------|--|
| Revisio | n History: 2006-10-17, V2.1                  |
| Previou | s Version: 2006-03-16                        |
| Page    | Subjects (major changes since last revision) |
| All     | Layout conformation                          |
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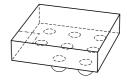


### **Microphone Filter and ESD Protection**

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### **Feature**

- · Microphone filter
- Integrated ESD protection up to 15 kV
- · Low input impedance
- More than 30 dB stopband attenuation
- Ideal for GSM/UMTS
- Wafer Level Package with SnAgCu-Bumps



WLP-8-1,- 2, -4

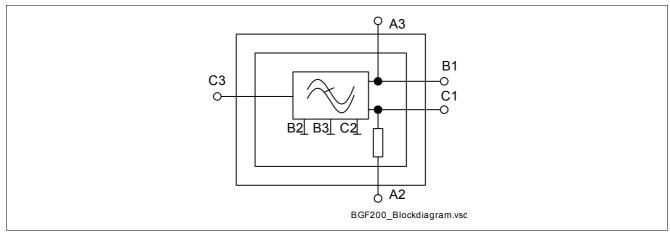


Figure 1 Blockdiagram

### **Description**

The BGF200 is a microphone filter with low pass characteristic offering a very high stop band attenuation up to 6 GHz. All pins are protected against ESD. The wafer level package is a green package with a size of only 1.6 mm  $\times$  1.6 mm and a total height of 0.65 mm.

| Туре   | Package | Marking | Chip  |
|--------|---------|---------|-------|
| BGF200 | WLP-8-4 | GF200   | N0703 |

Table 1 Maximum Ratings

| Parameter                                      | Symbol                    | Values |      |      | Unit | Note /                 |
|--|---------------------------|--------|------|------|------|------------------------|
|  |                           | Min.   | Тур. | Max. |      | <b>Test Condition</b>  |
| Voltage at pin A2 to GND                       | $V_{A2}$                  | 0      |      | 4.0  | V    |                        |
| Voltage at all other pins to GND               | $V_{P}$                   | -14    |      | 14   | V    |                        |
| Operating temperature range                    | $T_{OP}$                  | -40    |      | +85  | °C   |                        |
| Storage temperature range                      | $T_{STG}$                 | -65    |      | +150 | °C   |                        |
| Summed up input power for all pins             | $P_{IN}$                  |        |      | 25   | mW   | T <sub>A</sub> < 70 °C |
| <b>Electrostatic Discharge According to II</b> | EC61000-4-2 <sup>1)</sup> |        |      |      |      |                        |
| Between pins C3 and B3                         | $V_{E}$                   | -15    |      | 15   | kV   |                        |
| Between all other pins                         | $V_1$                     | -2     |      | 2    | kV   |                        |

<sup>1)</sup> Contact discharge



### **Microphone Filter and ESD Protection**

Table 2 Electrical Characteristics<sup>1)</sup>

| Parameter  | Symbol        | Values |      |      | Unit | Note /  |
|--|---------------|--------|------|------|------|---|
|  |               | Min.   | Тур. | Max. |      | <b>Test Condition</b>                                       |
| Resistors $R_1$ , $R_2$ , $R_4$                          | $R_{1,2,4}$   | 2090   | 2200 | 2310 | Ω    |   |
| Resistor $R_3$ , $R_5$                                   | $R_{3,5}$     | 47.5   | 50   | 52.5 | Ω    |   |
| Capacitances $C_1, C_2, C_3, C_4$                        | $C_{1,2,3,4}$ | 800    | 1000 | 1350 | pF   |   |
| Capacitances C <sub>5</sub>                              | $C_5$         | 120    | 150  | 200  | pF   |   |
| Substrate leakage currents all pins to GND               | I             |        |      | 100  | nA   | V <sub>R</sub> = 3 V  |
| Insertion loss <sup>2)</sup> pins $C_3$ to $B_1$ , $C_1$ | IL            | 30     |      |      | dB   | F = 0.1 6  GHz<br>$Z_{\text{S}} = Z_{\text{L}} = 50 \Omega$ |

<sup>1)</sup> at  $T_{A} = 25 \, ^{\circ}\text{C}$ 

2)Insertion loss (see also Figure 3) strongly depends upon source and load impedance. For RF test purposes a 50  $\Omega$  environment is used.

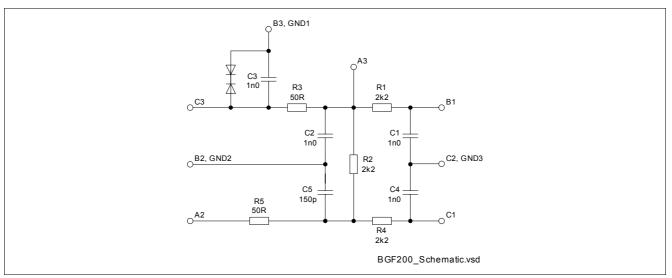


Figure 2 Schematic



### **Microphone Filter and ESD Protection**

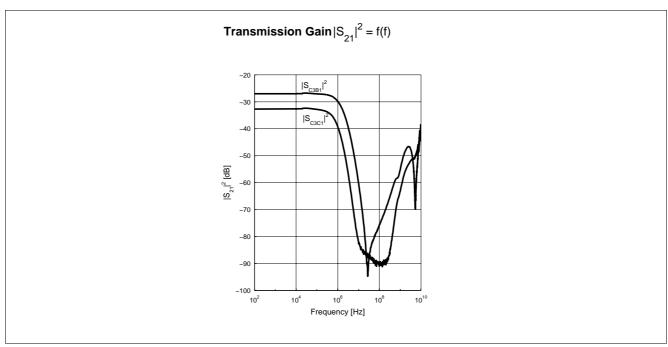


Figure 3 Transmission C3 - B1, C3 - C1

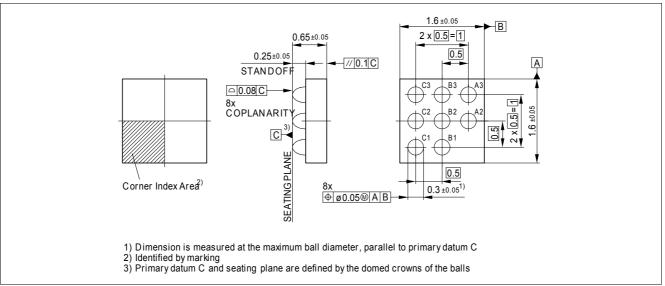


Figure 4 Package Outline WLP-8-4

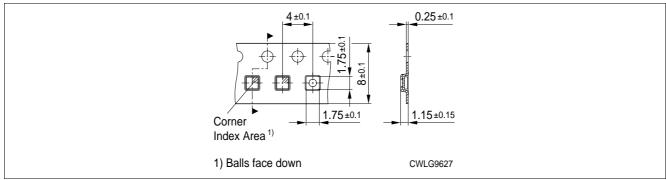


Figure 5 Tape for WLP-8-4